

# EMIF02-USB01F2

# 2-line IPAD<sup>™</sup>, EMI filter including ESD protection

### Features

- 2-line low-pass filter + ESD protection
- High efficiency in EMI filtering
- Lead-free package
- Very low PCB space occupation: < 2.5 mm<sup>2</sup>
- Very thin package: 0.65 mm
- High efficiency in ESD suppression (IEC 61000-4-2 level 4)
- High reliability offered by monolithic integration
- High reduction of parasitic elements through integration and wafer level packaging

#### Complies with the following standards

- IEC 61000-4-2 level 4
  - ±15 kV (air discharge)
  - ±8 kV (contact discharge)

### Application

■ ESD protection and EMI filtering for USB port

### Description

The EMIF02-USB01F2 is a highly integrated array designed to suppress EMI / RFI noise for USB port filtering. The EMIF02-USB01F2 Flip-Chip packaging means the package size is equal to the die size.

Additionally, this filter includes ESD protection circuitry which prevents damage to the protected device when subjected to ESD surges up to 15 kV.











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# 1 Characteristics

Table 1.	Absolute	ratings	(T <sub>amb</sub> =	: 25 °C)

Symbol	Parameter	Value	Unit
Тj	Junction temperature	125	°C
T <sub>op</sub>	Operating temperature range	-40 to +85	°C
T <sub>stg</sub>	Storage temperature range	-55 to 150	°C

#### Figure 3. Electrical characteristics - definitions



Table 2.	Electrical characteristics - values (T <sub>amb</sub> = 25 °C)
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Symbol	Test conditions	Min.	Тур.	Max.	Unit
V <sub>BR</sub> I <sub>R</sub> = 1 mA		6	-	-	V
I <sub>RM</sub>	V <sub>RM</sub> = 3 V	-	-	0.5	μA
C <sub>line</sub>	@ 0 V	-	40	45	pF
R <sub>1</sub> , R <sub>2</sub>	Tolerance ± 5 %	-	33	-	Ω
R <sub>3</sub> Tolerance ± 5 %		-	1.30	-	kΩ



#### Figure 4. S21 (dB) attenuation measurement Figure 5. Analog crosstalk measurements











# 2 Application information

#### A1, A3, B2, C1, C3, E1, E3 R 1k3 3.8nH 02 Cbump Rsubump C1O () A3 C 1/00 Obulk 0.15nH Lbump 1k3 D02\_usb 100m bulk Rbump R\_33F 0.23nH 0.15nH D02\_usb 🛓 **↓** D02\_Nw Lhole ₽ cap\_33R Csub cap\_33R Csub Rhole rsub\_33R Rsub\_D Lgnd\_D 0 D2 Q 0.7nH ------OE3 0.3nH E1Ocap\_33R cap\_33R Csub Csub rsub\_33R 33B rsub\_

#### Figure 9. Aplac model (resistors, diodes and bumps and ground connections)

#### Figure 10. Aplac model parameters

R_33R 33.9 cap_33R 1.2pF	Model D02_Nw BV=100 IBV=1m	Model D02_usb BV=16 IBV=1m
R_1k3 1.3k	CJO=6.8p M=0.3333	CJO=Cz M=0.3333
Cz29pF Rsub_D 100	RS=2 VJ=0.6 TT=100n	RS=2 VJ=0.6 TT=100n
Csub0.3pF		
Rsub_33R 15 Rsub_1k3 50		
lhole10pH Rhole400m Copholo0.4pE		
Lgnd_D 150pH		
Lbump50pH		
Cbump1.5pF		
Rsubump150		
	cap_33R 1.2pF R_1k3 1.3k Cz29pF Rsub_D 100 Csub0.3pF Rsub_33R 15 Rsub_1k3 50 hole10pH Rhole400m Caphole0.4pF Lgnd_D 150pH Lbump50pH Rbump50pH Rbump50m Cbump1.5pF	bag_33R 1.2pF BV=100 IBV=1m   R_1k3 1.3k CJ0=6.8p M=0.3333   Cz29pF VJ=0.6 TT=100n   Csub0.3pF Rsub_33R 15 Rsub_33R 15   Rsub_1k3 50 Hole10pH   hole10pH Role400m   Caphole0.4pF Lgnd_D 150pH   Lbump50pH Rbump50m   Cbump1.5pF Comparison



# **3** Ordering information scheme

Figure 11. Ordering information scheme

EMI filter		
Number of lines		
Information		
x = resistance value (Ohms)		
z = capacitance value / 10(pF)		
or		
3 letters = application		
2 digits = version		
Package		
F = Flip Chip		
X = 2: lead-free, pitch = 500 $\mu$ m, b	oump = 315 µm	

# 4 Package information

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In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK<sup>®</sup> is an ST trademark.



Figure 12. Package dimensions



#### Figure 15. Flip Chip tape and reel specification



# 5 Ordering information

#### Table 3. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
EMIF02-USB01F2	FF	Flip Chip	3.35 mg	5000	Tape and reel 7"

Note: More information is available in the application notes: AN1235:"Flip Chip: Package description and recommendations for use" AN1751: "EMI filters: Recommendations and measurements"



# 6 Revision history

### Table 4.Document revision history

Date	Revision	Changes
26-Oct-2004	1	Initial release.
16-Apr-2007	2	Updated ECOPACK statement. Updated <i>Figure 11</i> , <i>Figure 12</i> and <i>Figure 15</i> . Reformatted to current standards.
29-Apr-2008	3	Typographical errors corrected.
18-Sep-2009	4	Updated ESD graphic in <i>Figure 6</i> and <i>Figure 7</i> .



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