



## Product Change Notification / SYST-300FRI953

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**Date:**

01-Oct-2019

**Product Category:**

32-bit Microcontrollers

**PCN Type:**

Document Change

**Notification Subject:**

Data Sheet - PIC32MX5XX/6XX/7XX Family Data Sheet

**Affected CPNs:**

[SYST-300FRI953\\_Affected\\_CPN\\_10012019.pdf](#)

[SYST-300FRI953\\_Affected\\_CPN\\_10012019.csv](#)

**Notification Text:**

SYST-300FRI953

Microchip has released a new Product Documents for the PIC32MX5XX/6XX/7XX Family Data Sheet of devices. If you are using one of these devices please read the document located at [PIC32MX5XX/6XX/7XX Family Data Sheet](#).

**Notification Status:** Final

**Description of Change:** This revision includes typographical and formatting updates throughout the data sheet text. In addition, all SFR Register maps were moved from the Memory chapter to their respective peripheral chapters. All other major updates are referenced by their respective section in Table B-8.

**TABLE B-8: MAJOR SECTION UPDATES**

Section Name	Update Description
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7.0 "Interrupt Controller"	Updated the U3 and U2 bit names in the following Register Maps: a) TABLE 7-2: "Interrupt Register Map for PIC32MX534F064H, PIC32MX564F064H, PIC32MX564F128H, PIC32MX575F256H and PIC32MX575F512H Devices" b) TABLE 7-3: "Interrupt Register Map for PIC32MX664F064H, PIC32MX664F128H, PIC32MX675F256H, PIC32MX675F512H and PIC32MX695F512H Devices" c) TABLE 7-4: "Interrupt Register Map for PIC32MX764F128H, PIC32MX775F256H, PIC32MX775F512H and PIC32MX795F512H Devices" d) TABLE 7-5: "Interrupt Register Map for PIC32MX534F064L, PIC32MX564F064L, PIC32MX564F128L PIC32MX575F512L and PIC32MX575F256L Devices" e) TABLE 7-6: "Interrupt Register Map for PIC32MX664F064L, PIC32MX664F128L, PIC32MX675F256L, PIC32MX675F512L and PIC32MX695F512L Devices" f) TABLE 7-7: "Interrupt Register Map for PIC32MX764F128L, PIC32MX775F256L, PIC32MX775F512L and PIC32MX795F512L Devices"
18.0 "Serial Peripheral Interface (SPI)"	Updated the FRMPOL bit description in Register 18-1: "SPIx CON: SPI Control Register"
20.0 "Universal Asynchronous Receiver Transmitter (UART)"	Updated FIGURE 20-3: "Transmission (8-bit or 9-bit Data)"
21.0 "Parallel Master Port (PMP)"	Updated bit 10 for Register 21-2: "PMMODE: Parallel Port Mode Register"

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Manufacturability

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 1 October 2019

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

[PIC32MX5XX/6XX/7XX Family Data Sheet](#)

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